JC525 U.S. PTO
09/666528
09/18/00

Subclass 51
Class 365
ISSUE CLASSIFICATION

JC2
JC525

U.S. UTILITY Patent Application

	O.I.P.E.	PATENT DATE
	∩C ∧m² SCANNED Q.A. A.G.	NOV 26 2002

APPLICATION NO.	CONT/PRIOR	CLASS 365 714 711	SUBCLASS 51	ART UNIT 2104 2818	EXAMINER
09/666528					HOANG, HUAN

APPLICANTS

John Halbert
Randy Bonella

TITLE

Memory module having buffer for isolating stacked memory devices

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
365	51	365	52	63				
INTERNATIONAL CLASSIFICATION								
G 1 1 C	5/02							
					☐ Continued on Issue Slip Inside File Jacket			

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	2	4	4	26	1

☐ The term of this patent subsequent to _____ (date) has been disclaimed.		NOTICE OF ALLOWANCE MAILED
	(Assistant Examiner) (Date)	8/29/02
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____		ISSUE FEE (W)
_____	HUAN HOANG PRIMARY EXAMINER 8/26/02 (Primary Examiner) (Date)	Amount Due: 128000	Date Paid: 10-16-02
☐ The terminal ___ months of this patent have been disclaimed.	(Legal Instruments Examiner) 9/18/02 (Date)	ISSUE BATCH NUMBER

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Formal Drawings (shts) set _____

(FACE)